



RECEIVED

MAY 24 2002

PATENT
P56181

Technology Center 2100

BT
S-29-0

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JU-HEON LEE

Serial No.: 09/685,138

Examiner: GAFFIN, JEFFREY A.

Filed: 11 October 2000

Art Unit: 2182

For: PORTABLE INTEGRATED CIRCUIT MEMORY DEVICE FOR USE WITH
UNIVERSAL SERIAL BUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites,
describes, and provides a copy of the following art reference:

U.S. Patent No. 6,148,354 to Ban *et al.*, entitled *ARCHITECTURE FOR A UNIVERSAL
SERIAL BUS-BASED PC FLASH DISK*, issued on 14 November 2000 which pertains to flash
memory used in conjunction with a USB controller.

The citation of the foregoing reference is not intended to constitute an assertion that other or
more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging
and thorough search of the relevant art.

No fee is incurred by this Statement.

Respectfully submitted,



Robert E. Bushnell
Reg. No.: 27,774

1522 "K" Street, N.W., Suite 300
Washington, D.C. 20005
Area Code: 202-638-5740

Folio: P56181
Date: 22 May 2002
I.D.: REB/kf